**Advanced Embedded Systems (ECGR4101/5101) Lab Demonstration Validation Sheet**

This sheet should be modified by the student to reflect the current lab assignment being demonstrated

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| --- | --- |
| Lab Number: | Lab 2 – ADC |
| Team Members | |  | | --- | | Team Member 1: | | Team Member 2: | |
| Date: |  |

# Lab Requirements

|  |  |  |  |
| --- | --- | --- | --- |
| REQ Number | Objective | Self-Review | TA  Review |
| 1 | ADC voltages are properly read from POT |  |  |
| 2 | ADC voltages are properly read from photo resistor |  |  |
| 3 | Input voltage to display is toggled by pressing the SW2 |  |  |
| 4 | The LED Bar lights up and can be controlled properly |  |  |
| 5 | The ADC Value is scaled properly and displayed on the LED Bar (one LED at a time, with no oscillations) |  |  |
| 6 | Grade will also be determined by the quality of the board soldering work (turn in the board in the bag, stapled to the check-off sheet). |  |  |